## Annex B



## List of research specialisms for Sub-panel 11: Computer Sciences and Informatics

This REF topic classification is taken from the Association of Computing Machinery (ACM) Computing Classification System March 2012 Revision, hence the use of American spelling.

Please have the REF topic classification number enclosed in '< >' as the first characters in the topic descriptor, followed by a text topic descriptor. If the output relates to more than one topic, please select the one to which the contribution is the greatest. Since the purpose is to assess whether we have sufficient expertise (and later, to assign outputs to sub-panel members), please use only a single topic. If there are many sub-topics which apply equally, then choose one at the highest level.

We think the ACM classification will apply to anything we have assessed in the past – from art to quantum physics – but we have allowed for 'Other topics'.

As noted, the information will enable the sub-panel to determine whether there are topics for which they do not have sufficient expertise, and a similar classification will be used to allocate outputs to sub-panel members for review. We realize this is a very coarse grained classification but a finer grained classification is impractical. This classification is only an aid to the sub-panel and will not affect assessment of outputs in any way.

## Table of Contents

Hardware	3
Computer systems organization	7
Networks	8
Software and its engineering	10
Theory of computation	14
Mathematics of computing	18
Information systems	20
Security and privacy	26
Human-centered computing	28
Computing methodologies	30
Applied computing	35
Social and professional topics	38
Network operations	38
Any other topics	39

ACM 2012 Topics	REF Classification
Hardware	1
Printed circuit boards	1
Electromagnetic interference and compatibility	1
PCB design and layout	1
Communication hardware, interfaces and storage	1
Signal processing systems	1
Digital signal processing	1
Beamforming	1
Noise reduction	1
Sensors and actuators	1
Buses and high-speed links	1
Displays and imagers	1
External storage	1
Networking hardware	1
Printers	1
Sensor applications and deployments	1
Sensor devices and platforms	1
Sound-based input / output	1
Tactile and hand-based interfaces	1
Touch screens	1
Haptic devices	1
Scanners	1
Wireless devices	1
Wireless integrated network sensors	1
Electro-mechanical devices	1
Integrated circuits	1
3D integrated circuits	1
Interconnect	1
Input / output circuits	1
Metallic interconnect	1
Photonic and optical interconnect	1
Radio frequency and wireless interconnect	1
Semiconductor memory	1
Dynamic memory	1
Static memory	1
Non-volatile memory	1
Read-only memory	1
Digital switches	1
Transistors	1
Logic families	1
Logic circuits	1
Arithmetic and datapath circuits	1
Annihietic and datapath circuits	1
Combinational circuits	1
Design modules and hierarchy	1
Finite state machines	-
	1
Sequential circuits	1
Reconfigurable logic and FPGAs	1
Hardware accelerators	1
High-speed input / output	1
Programmable logic elements	1

	4
Programmable interconnect	1
Reconfigurable logic applications	1
Very large scale integration design	1
3D integrated circuits	1
Analog and mixed-signal circuits	1
Data conversion	1
Clock generation and timing	1
Analog and mixed-signal circuit optimization	1
Radio frequency and wireless circuits	1
Wireline communication	1
Analog and mixed-signal circuit synthesis	1
Application-specific VLSI designs	1
Application specific integrated circuits	1
Application specific instruction set processors	1
Application specific processors	1
Design reuse and communication-based design	1
Network on chip	1
System on a chip	1
Platform-based design	1
Hard and soft IP	1
Design rules	1
Economics of chip design and manufacturing	1
Full-custom circuits	1
VLSI design manufacturing considerations	1
On-chip resource management	1
On-chip sensors	1
Standard cell libraries	1
VLSI packaging	1
Die and wafer stacking	1
Input / output styles	1
Multi-chip modules	1
Package-level interconnect	1
VLSI system specification and constraints	1
Power and energy	1
Thermal issues	1
Temperature monitoring	1
Temperature simulation and estimation	1
Temperature control	1
Temperature optimization	1
Energy generation and storage	1
Batteries	1
Fuel-based energy	1
Renewable energy	1
Reusable energy storage	1
Energy distribution	1
Energy metering	1
Power conversion	1
Power networks	1
Smart grid	1
Impact on the environment	1
	1
Power estimation and optimization	1
Switching devices power issues	
Interconnect power issues	1
Circuits power issues	1
Chip-level power issues	1

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Platform power issues	1
Enterprise level and data centers power issues	1
Electronic design automation	1
High-level and register-transfer level synthesis	1
Datapath optimization	1
Hardware-software codesign	1
Resource binding and sharing	1
Operations scheduling	1
Hardware description languages and compilation	1
Logic synthesis	1
Combinational synthesis	1
Circuit optimization	1
Sequential synthesis	1
Technology-mapping	1
Transistor-level synthesis	1
Modeling and parameter extraction	1
Physical design (EDA)	1
Clock-network synthesis	1
Packaging	1
Partitioning and floorplanning	1
Placement	1
Physical synthesis	I
	1
Power grid design	
Wire routing	1
Timing analysis	1
Electrical-level simulation	1
Model-order reduction	1
Compact delay models	1
Static timing analysis	1
Statistical timing analysis	1
Transition-based timing analysis	1
Methodologies for EDA	1
Best practices for EDA	1
Design databases for EDA	1
Software tools for EDA	1
Hardware validation	1
Functional verification	1
Model checking	1
Coverage metrics	1
Equivalence checking	1
Semi-formal verification	1
Simulation and emulation	1
Transaction-level verification	1
	1
Theorem proving and SAT solving	
Assertion checking	1
Physical verification	1
Design rule checking	1
Layout-versus-schematics	1
Power and thermal analysis	1
Timing analysis and sign-off	1
Post-manufacture validation and debug	1
Bug detection, localization and diagnosis	1
Bug fixing (hardware)	1
Design for debug	1
Hardware test	1
	1

Analog, mixed-signal and radio frequency test	1
Board- and system-level test	1
Defect-based test	1
Design for testability	1
Built-in self-test	1
Online test and diagnostics	1
Test data compression	1
Fault models and test metrics	1
Memory test and repair	1
Hardware reliability screening	1
Test-pattern generation and fault simulation	1
Testing with distributed and parallel systems	1
Robustness	1
Fault tolerance	1
Error detection and error correction	1
Failure prediction	1
	1
Failure recovery, maintenance and self-repair Redundancy	1
	1
Self-checking mechanisms	
System-level fault tolerance	1
Design for manufacturability	1
Process variations	1
Yield and cost modeling	1
Yield and cost optimization	1
Hardware reliability	1
Aging of circuits and systems	1
Circuit hardening	1
Early-life failures and infant mortality	1
Process, voltage and temperature variations	1
Signal integrity and noise analysis	1
Transient errors and upsets	1
Safety critical systems	1
Emerging technologies	1
Analysis and design of emerging devices and systems	1
Emerging architectures	1
Emerging languages and compilers	1
Emerging simulation	1
Emerging tools and methodologies	1
Biology-related information processing	1
Bio-embedded electronics	1
Neural systems	1
Circuit substrates	1
III-V compounds	1
Carbon based electronics	1
Cellular neural networks	1
Flexible and printable circuits	1
Superconducting circuits	1
Electromechanical systems	1
Microelectromechanical systems	1
Nanoelectromechanical systems	1
Emerging interfaces	1
Memory and dense storage	1
Emerging optical and photonic technologies	1
Reversible logic Plasmonics	1
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Quantum technologies	1
Single electron devices	1
Tunneling devices	1
Quantum computation	1
Quantum communication and cryptography	1
Quantum error correction and fault tolerance	1
Quantum dots and cellular automata	1
Spintronics and magnetic technologies	1
Computer systems organization	
Architectures	2
Serial architectures	2
Reduced instruction set computing	2
Complex instruction set computing	2
Superscalar architectures	2
Pipeline computing	2
Stack machines	2
Parallel architectures	2
Very long instruction word	2
Interconnection architectures	2
Multiple instruction, multiple data	2
Cellular architectures	2
Multiple instruction, single data	2
Single instruction, multiple data	2
Systolic arrays	2
Multicore architectures	2
Distributed architectures	2
Cloud computing	2
Client-server architectures	2
n-tier architectures	2
Peer-to-peer architectures	2
Grid computing	2
Other architectures	2
Neural networks	2
Reconfigurable computing	2
Analog computers	2
Data flow architectures	2
Heterogeneous (hybrid) systems	2
Self-organizing autonomic computing	2
Optical computing	2
Quantum computing	
Molecular computing	2
High-level language architectures	2
Special purpose systems	2
Embedded and cyber-physical systems	
Sensor networks	2
Robotics	2
Robotic components	2
Robotic control	2
Robotic autonomy External interfaces for robotics	2
	2
Sensors and actuators	2
System on a chip	2
Embedded systems	<u>ک</u>

Firmware	
Embedded hardware	2
Embedded software	2
Real-time systems	3
Real-time operating systems	3
Real-time languages	3
Real-time system specification	3
Real-time system architecture	3
Dependable and fault-tolerant systems and networks	3
Reliability	3
Availability	3
Maintainability and maintenance	3
Processors and memory architectures	3
Secondary storage organization	3
Redundancy	3
Fault-tolerant network topologies	3
Networks	
Network architectures	4
Network design principles	4
Layering	4
Naming and addressing	4
Programming interfaces	4
Network protocols	4
Network protocol design	4
Protocol correctness	4
Protocol testing and verification	4
Formal specifications	4
Link-layer protocols	4
Network layer protocols	4
	4
Routing protocols	
Signaling protocols	4
Transport protocols	4
Session protocols	4
Presentation protocols	4
Application layer protocols	4
Peer-to-peer protocols	4
OAM protocols	4
Time synchronization protocols	4
Network policy	4
Cross-layer protocols	4
Network File System (NFS) protocol	4
Network components	4
Intermediate nodes	4
Routers	4
Bridges and switches	4
Physical links	4
Repeaters	4
Middle boxes / network appliances	4
End nodes	4
Network adapters	4
Network servers	
	4
Wireless access points, base stations and infrastructure	4
Cognitive radios	4

Logical nodes	4
Network domains	4
Network algorithms	5
Data path algorithms	5
Packet classification	5
Deep packet inspection	5
Packet scheduling	5
Control path algorithms	5
Network resources allocation	5
Network control algorithms	5
Traffic engineering algorithms	5
Network design and planning algorithms	5
Network economics	5
Network performance evaluation	5
Network performance modeling	5
Network simulations	5
Network experimentation	5
Network performance analysis	5
Network measurement	5
Network properties	6
Network security	6
Security protocols	6
Web protocol security	6
Mobile and wireless security	6
Denial-of-service attacks	6
Firewalls	6
Network range	6
Short-range networks	6
Local area networks	6
Metropolitan area networks	6
Wide area networks	6
Very long-range networks	6
Network structure	6
Topology analysis and generation	6
Physical topologies	6
Logical / virtual topologies	6
Network topology types	6
Point-to-point networks	6
Bus networks	6
Star networks	6
Ring networks	6
Token ring networks	6
Fiber distributed data interface (FDDI)	6
Mesh networks	6
Wireless mesh networks	6
Hybrid networks	6
Network dynamics	6
Network reliability	6
Error detection and error correction	6
Network mobility	6
Network manageability	6
Network privacy and anonymity	6
Network services	6
Naming and addressing	6
Cloud computing	6
	I

Location based services	6
Programmable networks	6
In-network processing	6
Network management	6
Network monitoring	6
Network types	6
Network on chip	6
Home networks	6
Storage area networks	6
Data center networks	6
Wired access networks	6
Cyber-physical networks	6
Sensor networks	6
Mobile networks	6
Overlay and other logical network structures	6
	6
Peer-to-peer networks World Wide Web (network structure)	6
Social media networks	
	6
Online social networks	6
Wireless access networks	6
Wireless local area networks	6
Wireless personal area networks	6
Ad hoc networks	6
Mobile ad hoc networks	6
Public Internet	6
Packet-switching networks	6
Software and its engineering	
Software organization and properties	7
Contextual software domains	7
E-commerce infrastructure	7
Software infrastructure	7
Interpreters	7
Middleware	7
Message oriented middleware	7
Reflective middleware	7
Embedded middleware	7
	7
Virtual machines	
Virtual machines	
Operating systems	7
Operating systems File systems management	7 7
Operating systems File systems management Memory management	7 7 7 7
Operating systems File systems management Memory management Virtual memory	7 7 7 7 7
Operating systems File systems management Memory management Virtual memory Main memory	7 7 7 7 7 7 7
Operating systems File systems management Memory management Virtual memory Main memory Allocation / deallocation strategies	7 7 7 7 7 7 7 7
Operating systems File systems management Memory management Virtual memory Main memory Allocation / deallocation strategies Garbage collection	7 7 7 7 7 7 7 7 7 7
Operating systems File systems management Memory management Virtual memory Main memory Allocation / deallocation strategies Garbage collection Distributed memory	7 7 7 7 7 7 7 7 7 7 7 7
Operating systems         File systems management         Memory management         Virtual memory         Main memory         Allocation / deallocation strategies         Garbage collection         Distributed memory         Secondary storage	7 7 7 7 7 7 7 7 7 7 7 7 7 7
Operating systems         File systems management         Memory management         Virtual memory         Main memory         Allocation / deallocation strategies         Garbage collection         Distributed memory         Secondary storage         Process management	7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7
Operating systems         File systems management         Memory management         Virtual memory         Main memory         Allocation / deallocation strategies         Garbage collection         Distributed memory         Secondary storage         Process management         Scheduling	7         7
Operating systems         File systems management         Memory management         Virtual memory         Main memory         Allocation / deallocation strategies         Garbage collection         Distributed memory         Secondary storage         Process management         Scheduling         Deadlocks	7         7
Operating systems         File systems management         Memory management         Virtual memory         Main memory         Allocation / deallocation strategies         Garbage collection         Distributed memory         Secondary storage         Process management         Scheduling         Deadlocks         Multithreading	7         7 <td< td=""></td<>
Operating systems         File systems management         Memory management         Virtual memory         Main memory         Allocation / deallocation strategies         Garbage collection         Distributed memory         Secondary storage         Process management         Scheduling         Deadlocks	7         7

Mutual exclusion	7
Concurrency control	7
Power management	7
Process synchronization	7
Communications management	7
Buffering	7
Input / output	7
	7
Message passing Virtual worlds software	7
Interactive games	7
Virtual worlds training simulations	7
Software system structures Embedded software	7
	7
Software architectures	
n-tier architectures	7
Peer-to-peer architectures	7
Data flow architectures	7
Cooperating communicating processes	7
Layered systems	7
Publish-subscribe / event-based architectures	7
Electronic blackboards	7
Simulator / interpreter	7
Object oriented architectures	7
Tightly coupled architectures	7
Space-based architectures	7
3-tier architectures	7
Software system models	7
Petri nets	7
State systems	7
Entity relationship modeling	7
Model-driven software engineering	7
Feature interaction	7
Massively parallel systems	7
Ultra-large-scale systems	7
Distributed systems organizing principles	7
Cloud computing	7
Client-server architectures	7
Grid computing	7
Organizing principles for web applications	7
Real-time systems software	7
Abstraction, modeling and modularity	7
Software functional properties	7
Correctness	7
Synchronization	7
Functionality	7
Real-time schedulability	7
Consistency	7
Completeness	7
Access protection	7
Formal methods	7
	7
Model checking Software verification	7
	7
Automated static analysis	
Dynamic analysis	7
Extra-functional properties	1

Software performance       7         Software reliability       7         Software fault tolerance       7         Checkpoint / restart       7         Software safety       7         Software usability       7         Software usability       7         Software usability       7         Software notations and tools       8         General programming languages       8         Language types       8         Parallel programming languages       8         Distributed programming languages       8         Concurrent programming languages       8         Constraint and logi languages       8         Constraint and logi languages       8         Constraint and logi languages       8         Assembly languages       8         Multiparadigm languages       8	Interoperability	7
Software reliability     7       Software fault tolerance     7       Checkpoint / restart     7       Software safety     7       Software usability     7       Software notations and tools     8       General programming languages     8       Language types     8       Distributed programming languages     8       Distributed programming languages     8       Object oriented languages     8       Concurrent programming languages     8       Constraint and logic languages     8       Constraint and logic languages     8       Data flow languages     8       Multiparadigm languages     8       Assembly languages     8       Asstract data types     8       Abstract data types     8       Abstract data types     8       Control structures     8       Constraints     8       Constraints     8       Concurrent programming structures     8       Concoutines     8       Astra		
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Software usability       7         Software usability       7         Software usability       7         Software notations and tools       8         General programming languages       8         Parallel programming languages       8         Distributed programming languages       8         Imperative languages       8         Object oriented languages       8         Concurrent programming languages       8         Constraint and logic languages       8         Constraint and logic languages       8         Extensible languages       8         Multiparadigm languages       8         Mostant data types       8         Abstract data types       8         Abstract data types       8         Control structures       8         Control structures       8         Constraints       8         Concurrent programming structures       8         Concourrent programming structures       8		
Software usability         7           Software notations and tools         8           General programming languages         8           Language types         8           Parallel programming languages         8           Distributed programming languages         8           Object oriented languages         8           Concurrent programming languages         8           Constraint and logic languages         8           Assembly languages         8           Multiparadigm languages         8           Multiparadigm languages         8           Abstract data types         8           Abstract data types         8           Abstract data types         8           Constraints         8           Procedures, functions and sub		
Software notations and tools         8           General programming languages         8           Language types         8           Parallel programming languages         8           Distributed programming languages         8           Imperative languages         8           Object oriented languages         8           Concurrent programming languages         8           Concurrent programming languages         8           Constraint and logic languages         8           Constraint and logic languages         8           Extensible languages         8           Assembly languages         8           Multiparadigm languages         8           Assembly languages         8           Abstract data types         8           Abstract data types         8           Control structures         8           Data types and structures         8           Control structures         8           Constraints         8           Constraints         8           Control structures         8           Control structures         8           Constraints         8           Concurrent programming structures         8      <		
General programming languages       8         Language types       8         Parallel programming languages       8         Distributed programming languages       8         Object oriented languages       8         Concurrent programming languages       8         Concurrent programming languages       8         Constraint and logic languages       8         Constraint and logic languages       8         Extensible languages       8         Assembly languages       8         Multiparadigm languages       8         Multiparadigm languages       8         Assembly languages       8         Assembly languages       8         Multiparadigm languages       8         Abstract data types       8         Abstract data types       8         Polymorphism       8         Inheritance       8         Constraints       8         Constraints       8         Constraints       8         Constraints       8         Contraints       8         Constraints       8         Packages       8         Concurrent programming structures       8		
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Parallel programming languages       8         Distributed programming languages       8         Imperative languages       8         Object oriented languages       8         Concurrent programming languages       8         Concurrent programming languages       8         Constraint and logic languages       8         Data flow languages       8         Extensible languages       8         Multiparadigm languages       8         Very high level languages       8         Language features       8         Abstract data types       8         Polymorphism       8         Inheritance       8         Constraints       8         Classes and objects       8         Classes and objects       8         Constraints       8         Recursion       8         Concurrent programming structures       8         Procedures, functions and subroutines       8         Protemers       8         Concurrent programming structures       8         Proteines       8         Concurrent programming structures       8         Procedures, functions and subroutines       8         Syi		
Distributed programming languages     8       Imperative languages     8       Object oriented languages     8       Concurrent programming languages     8       Constraint and logic languages     8       Data flow languages     8       Data flow languages     8       Assembly languages     8       Multiparadigm languages     8       Multiparadigm languages     8       Language features     8       Abstract data types     8       Polymorphism     8       Control structures     8       Constraints     8       Constraints     8       Constraints     8       Constraints     8       Concurrent programming structures     8       Concurrent programming structures     8       Concurrent programming structures     8       Polymorphism     8       Control structures     8       Constraints     8       Constraints     8       Recursion     8       Concurrent programming structures     8       Procedures, functions and subroutines     8       Pormal language definitions     8       Syntax     8       Semantics     8       Compilers     8    <		
Imperative languages     8       Object oriented languages     8       Functional languages     8       Concurrent programming languages     8       Data flow languages     8       Data flow languages     8       Extensible languages     8       Multiparadigm languages     8       Very high level languages     8       Language features     8       Abstract data types     8       Polymorphism     8       Inheritance     8       Constraints     8       Control structures     8       Constraints     8       Constraints     8       Control structures     8       Constraints     8       Constraints     8       Constraints     8       Constraints     8       Constraints     8       Constraints     8       Concurrent programming structures     8       Procedures, functions and subroutines     8       Procedures, functions and subroutines     8       Formal language definitions     8       Syntax     8       Semantics     8       Compilers     8       Interpreters     8       Interpreters     8 <t< td=""><td></td><td></td></t<>		
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Functional languages8Concurrent programming languages8Constraint and logic languages8Data flow languages8Extensible languages8Assembly languages8Multiparadigm languages8Very high level languages8Language features8Abstract data types8Polymorphism8Inheritance8Control structures8Control structures8Classes and objects8Modules / packages8Concurrent programming structures8Concurrent programming structures8Concurrent programming structures8Concurrent programming structures8Procedures, functions and subroutines8Frameworks8Formal language definitions8Syntax8Semantics8Compilers8Maguage definitions8Syntax8Semantics8Compilers8Astare definitions8Syntax8Semantics8Compilers8Dynamic compilers8Dynamic compilers8Constraine and compilers8Compilers8Constraines8Source code generation8Retargetable compilers8Retargetable compilers8Retargetable compilers8Constraine and compiler ge		
Concurrent programming languages8Constraint and logic languages8Data flow languages8Extensible languages8Assembly languages8Multiparadigm languages8Very high level languages8Language features8Abstract data types8Polymorphism8Inheritance8Control structures8Control structures8Constraints8Constraints8Constraints8Concurrent programming structures8Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Semantics8Compilers8Syntax8Semantics8Compilers8Interpreters8Duranic compilers8Dynamic compilers8Dynamic compilers8Dynamic compilers8Dynamic compilers8Corice of eneration8Retargetable compilers8Retargetable compilers8Retargetable compilers8Retargetable compilers8Rutime environments8Rutime environments8	Eurotional languages	
Constraint and logic languages8Data flow languages8Extensible languages8Assembly languages8Multiparadigm languages8Very high level languages8Language features8Abstract data types8Polymorphism8Inheritance8Control structures8Classes and objects8Modules / packages8Constraints8Concurrent programming structures8Concurrent programming structures8Concurrent programming structures8Patterns8Concurrent programming structures8Patterns8Concurrent programming structures8Patterns8Concurrent programming structures8Patterns8Concurrent programming structures8Semantics8Coroutines8Formal language definitions8Semantics8Compilers8Interpreters8Incremental compilers8Just-in-time compilers8Just-in-time compilers8Dynamic compilers8Currentic compilers8Retargetable compilers8Retargetable compilers8Rutime environments8Rutime environments8		
Data flow languages8Extensible languages8Assembly languages8Multiparadigm languages8Very high level languages8Language features8Abstract data types8Polymorphism8Inheritance8Control structures8Classes and objects8Modules / packages8Constraints8Recursion8Concurrent programming structures8Coroutines8Coroutines8Frameworks8Semantics8Coroutines8Formal language definitions8Semantics8Compilers8Semantics8Semantics8Source code generation8Compilers8Retargetable compilers8Attrastact and compiler generators8Attrastact and compilers8Attrastact and compilers8Attrastact and compilers8Attrastact and compilers8Attrastact and compilers8Attrastact and comp		
Extensible languages8Assembly languages8Multiparadigm languages8Very high level languages8Language features8Abstract data types8Polymorphism8Inheritance8Control structures8Control structures8Modules / packages8Constraints8Constraints8Constraints8Concurrent programming structures8Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Syntax8Syntax8Semantics8Interpreters8Interpreters8Compilers8Surtax8Semantics8Surtax8Source code generation8Translator writing systems and compiler generators8Runtime environments8Source code generation8Runtime environments8		
Assembly languages     8       Multiparadigm languages     8       Very high level languages     8       Language features     8       Abstract data types     8       Polymorphism     8       Inheritance     8       Control structures     8       Control structures     8       Modules / packages     8       Constraints     8       Concurrent programming structures     8       Concurrent programming structures     8       Patterns     8       Coroutines     8       Frameworks     8       Syntax     8       Semantics     8       Compilers     8       Interpreters     8       Interpreters     8       Monumental compilers     8       Semantics     8       Compilers     8       Retargetable compilers     8       Matting systems and compiler generators     8       Matting systems and compiler generators     8	0 0	
Multiparadigm languages8Very high level languages8Language features8Abstract data types8Polymorphism8Inheritance8Control structures8Data types and structures8Classes and objects8Modules / packages8Constraints8Concurrent programming structures8Concurrent programming structures8Concurrent programming structures8Procedures, functions and subroutines8Formal language definitions8Semantics8Semantics8Compilers8Semantics8Semantics8Semantics8Semantics8Suntax8Semantics8Dincremental compilers8Actargetable compilers8Dynamic compilers8Translator writing systems and compiler generators8Runtime environments8Runtime environments8		
Very high level languages8Language features8Abstract data types8Polymorphism8Inheritance8Control structures8Data types and structures8Classes and objects8Modules / packages8Constraints8Concurrent programming structures8Concurrent programming structures8Procedures, functions and subroutines8Coroutines8Frameworks8Syntax8Semantics8Compilers8Interpreters8Compilers8Suttine compilers8Suttin-time compilers8Mathematic compilers8Translator writing systems and compiler generators8Source code generation8Source code generation8Returgetable compilers8Runtime environments8Source code generation8Runtime environments8		
Language features       8         Abstract data types       8         Polymorphism       8         Inheritance       8         Control structures       8         Data types and structures       8         Classes and objects       8         Modules / packages       8         Constraints       8         Recursion       8         Concurrent programming structures       8         Procedures, functions and subroutines       8         Procedures, functions and subroutines       8         Farmeworks       8         Syntax       8         Syntax       8         Semantics       8         Interpreters       8         Incremental compilers       8         Incremental compilers       8         Just-in-time compilers       8         Dynamic compilers       8         Dynamic compilers       8         Translator writing systems and compiler generators       8         Source code generation       8         Runtime environments       8		
Abstract data types8Polymorphism8Inheritance8Control structures8Data types and structures8Classes and objects8Modules / packages8Constraints8Concurrent programming structures8Concurrent programming structures8Procedures, functions and subroutines8Procedures, functions and subroutines8Frameworks8Frameworks8Somatics8Semantics8Semantics8Semantics8Interpreters8Interpreters8Interpreters8Dynamic compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8Runtime environments8		
Polymorphism8Inheritance8Control structures8Data types and structures8Classes and objects8Modules / packages8Constraints8Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Syntax8Semantics8Compilers8Semantics8Semantics8Semantics8Surfax8Surfax8Interpreters8Interpreters8Just-in-time compilers8Just-in-time compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Inheritance8Control structures8Data types and structures8Classes and objects8Classes and objects8Modules / packages8Constraints8Constraints8Recursion8Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Formal language definitions8Syntax8Semantics8Interpreters8Interpreters8Interpreters8Just-in-time compilers8Just-in-time compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Control structures8Data types and structures8Classes and objects8Modules / packages8Constraints8Recursion8Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Syntax8Semantics8Compilers8Incremental compilers8Semantics8Dynamic compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Runtime environments8Source code generation8Runtime environments8Runtime environments8		
Data types and structures8Classes and objects8Modules / packages8Constraints8Recursion8Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Syntax8Semantics8Compilers8Interpreters8Semantics8Suppose8Semantics8Suppose8Suppose8Semantics8Semantics8Suppose8Suppose8Semantics8Suppose8Suppose8Suppose8Suppose8Suppose8Suppose8Suppose8Source code generation8Runtime environments8		
Classes and objects8Modules / packages8Constraints8Recursion8Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Formal language definitions8Syntax8Semantics8Compilers8Interpreters8Interpreters8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Runtime environments8Runtime environments8		
Modules / packages8Constraints8Recursion8Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Formal language definitions8Syntax8Compilers8Compilers8Interpreters8Interpreters8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Runtime environments8Source code generation8Runtime environments8		
Constraints8Recursion8Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Formal language definitions8Syntax8Semantics8Compilers8Interpreters8Incremental compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Recursion8Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Formal language definitions8Syntax8Semantics8Compilers8Interpreters8Incremental compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Runtime environments8Runtime environments8		
Concurrent programming structures8Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Formal language definitions8Syntax8Semantics8Compilers8Interpreters8Incremental compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Procedures, functions and subroutines8Patterns8Coroutines8Frameworks8Formal language definitions8Syntax8Semantics8Compilers8Interpreters8Incremental compilers8Quartical compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Patterns8Coroutines8Frameworks8Formal language definitions8Syntax8Semantics8Compilers8Interpreters8Incremental compilers8Retargetable compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Coroutines8Frameworks8Formal language definitions8Syntax8Semantics8Compilers8Interpreters8Incremental compilers8Retargetable compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Frameworks8Formal language definitions8Syntax8Semantics8Compilers8Interpreters8Incremental compilers8Retargetable compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Formal language definitions8Syntax8Semantics8Compilers8Interpreters8Incremental compilers8Retargetable compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Syntax8Semantics8Compilers8Interpreters8Incremental compilers8Retargetable compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Semantics8Compilers8Interpreters8Incremental compilers8Retargetable compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Compilers8Interpreters8Incremental compilers8Retargetable compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Interpreters8Incremental compilers8Retargetable compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Incremental compilers8Retargetable compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Retargetable compilers8Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Just-in-time compilers8Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Dynamic compilers8Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Translator writing systems and compiler generators8Source code generation8Runtime environments8		
Source code generation     8       Runtime environments     8		
Runtime environments 8		
Preprocessors 8		
	Preprocessors	
Parsers 8		
Context specific languages 8		
Markup languages 8		
Extensible Markup Language (XML) 8		
Hypertext languages 8	Hypertext languages	8

Contraction of Longevice good	0
Scripting languages	8
Domain specific languages	8
Specialized application languages	8
API languages	8
Graphical user interface languages	8
Window managers	8
Command and control languages	8
Macro languages	8
Programming by example	8
State based definitions	8
Visual languages	8
Interface definition languages	8
System description languages	8
Design languages	8
Unified Modeling Language (UML)	8
Architecture description languages	8
System modeling languages	8
Orchestration languages	8
Integration frameworks	8
Specification languages	8
Development frameworks and environments	8
Object oriented frameworks	8
	8
Software as a service orchestration systems	
Integrated and visual development environments	8
Application specific development environments	8
Software configuration management and version control systems	
Software libraries and repositories	8
Software maintenance tools	8
Software creation and management	9
Designing software	9
Requirements analysis	9
Software design engineering	9
Software design tradeoffs	9
Software implementation planning	9
Software design techniques	9
Software development process management	9
Software development methods	9
Rapid application development	9
Agile software development	9
Capability Maturity Model	9
Waterfall model	9
Spiral model	9
V-model	9
Design patterns	9
Risk management	9
	9
Software development techniques	9
Software prototyping	9
Object oriented development	
Flowcharts	9
Reusability	9
Software product lines	9
Error handling and recovery	9
Software verification and validation	9
Software prototyping	9
Operational analysis	9

Software defect analysis	9
Software testing and debugging	9
Fault tree analysis	9
Process validation	9
Walkthroughs	9
Pair programming	9
	9
Use cases	
Acceptance testing	9
Traceability	9
Formal software verification	9
Empirical software validation	9
Software post-development issues	9
Software reverse engineering	9
Documentation	9
Backup procedures	9
Software evolution	9
Software version control	9
Maintaining software	9
System administration	9
Collaboration in software development	9
Open source model	9
Programming teams	9
Theory of computation	
Models of computation	10
Computability	10
Lambda calculus	10
Turing machines	10
Recursive functions	10
Probabilistic computation	10
	10
Quantum computation theory	
Quantum complexity theory	10
Quantum communication complexity	10
Quantum query complexity	10
Quantum information theory	10
Interactive computation	10
Streaming models	10
Concurrency	10
Parallel computing models	10
Distributed computing models	10
Process calculi	10
Timed and hybrid models	10
Abstract machines	10
Formal languages and automata theory	10
Formalisms	10
Algebraic language theory	10
Rewrite systems	10
Automata over infinite objects	10
Grammars and context-free languages	10
Tree languages	10
Automata extensions	10
Transducers	10
Quantitative automata	10
Regular languages	10

Computational complexity and cryptography	10
Complexity classes	10
Problems, reductions and completeness	10
Communication complexity	10
Circuit complexity	10
Oracles and decision trees	10
Algebraic complexity theory	10
Quantum complexity theory	10
Proof complexity	10
Interactive proof systems	10
Complexity theory and logic	10
Cryptographic primitives	10
Cryptographic protocols	10
Logic	11
Logic and verification	11
Proof theory	11
Modal and temporal logics	11
Automated reasoning	11
Constraint and logic programming	11
Constructive mathematics	11
Description logics	11
Equational logic and rewriting	11
Finite Model Theory	11
Higher order logic	11
Linear logic	11
Programming logic	11
Abstraction	11
Verification by model checking	11
Type theory	11
Hoare logic	11
Separation logic	11
Design and analysis of algorithms	12
Graph algorithms analysis	12
Network flows	12
Sparsification and spanners	12
Shortest paths	12
Dynamic graph algorithms	12
Approximation algorithms analysis	12
Scheduling algorithms	12
Packing and covering problems	12
Routing and network design problems	12
Facility location and clustering	12
Rounding techniques	12
Stochastic approximation	12
Numeric approximation algorithms	12
Mathematical optimization	12
Discrete optimization	12
Network optimization	12
Continuous optimization	12
Linear programming	12
	12
Semidefinite programming Convex optimization	12
	12
Quasiconvex programming and unimodality	
Stochastic control and optimization	<u>    12</u> 12
Quadratic programming	12

Kernel methods	12
Support vector machines	12
Gaussian processes	12
Boosting	12
Bayesian analysis	12
Inductive inference	12
Online learning theory	12
Multi-agent learning	12
Models of learning	12
Query learning	12
Structured prediction	12
Reinforcement learning	12
Sequential decision making	12
Inverse reinforcement learning	12
Apprenticeship learning	12
Multi-agent reinforcement learning	12
Adversarial learning	12
Active learning	12
Semi-supervised learning	12
Markov decision processes	12
Regret bounds	12
Algorithmic game theory and mechanism design	12
Social networks	12
	12
Algorithmic game theory	12
Algorithmic mechanism design	12
Solution concepts in game theory	12
Exact and approximate computation of equilibria	12
Quality of equilibria Convergence and learning in games	12
Market equilibria	12
Computational pricing and auctions	12
Representations of games and their complexity	12
Network games	12
Network formation	12
Computational advertising theory	12
Database theory	12
Data exchange	12
	12
Data provenance Data modeling	12
· · · · · · · · · · · · · · · · · · ·	12
Database query languages (principles)	12
Database constraints theory	12
Database interoperability Data structures and algorithms for data management	12
Data structures and algorithms for data management Database query processing and optimization (theory)	12
	12
Data integration	12
Logic and databases	12
Theory of database privacy and security	12
Incomplete, inconsistent, and uncertain databases	
Semantics and reasoning	10 10
Program constructs	
Control primitives	10
Functional constructs	10
Object oriented constructs	10
Program schemes	10
Type structures	10

Program semantics	10
Algebraic semantics	10
Denotational semantics	10
Operational semantics	10
Axiomatic semantics	10
Action semantics	10
Categorical semantics	10
	10
Program reasoning Invariants	10
	10
Program specifications	10
Pre- and post-conditions	
Program verification	10
Program analysis	10
Assertions	10
Parsing	10
Abstraction	10
Mathematics of computing	
Mathematics of computing Discrete mathematics	13
Combinatorics	13
Combinatoric problems	13
Permutations and combinations	13
Combinatorial algorithms	13
Generating functions	13
Combinatorial optimization	13
Combinatorics on words	13
Enumeration	13
Graph theory	13
Trees	13
Hypergraphs	13
Random graphs	13
Graph coloring	13
Paths and connectivity problems	13
Graph enumeration	13
Matchings and factors	13
Graphs and surfaces	13
Network flows	13
Spectra of graphs	13
Extremal graph theory	13
Matroids and greedoids	13
Graph algorithms	13
Approximation algorithms	13
Probability and statistics	13
Probabilistic representations	13
Bayesian networks	13
Markov networks	13
Factor graphs	13
Decision diagrams	13
Equational models	13
Causal networks	13
Stochastic differential equations	13
Nonparametric representations	13
Kernel density estimators	13
Spline models	13
Bayesian nonparametric models	13

Probabilistic inference problems	13
Maximum likelihood estimation	13
Bayesian computation	13
	13
Computing most probable explanation Hypothesis testing and confidence interval computation	13
Density estimation	13
Quantile regression	13
Max marginal computation	13 13
Probabilistic reasoning algorithms Variable elimination	
	13
Loopy belief propagation	13
Variational methods	13
Expectation maximization	13
Markov-chain Monte Carlo methods	13
Gibbs sampling	13
Metropolis-Hastings algorithm	13
Simulated annealing	13
Markov-chain Monte Carlo convergence measures	13
Sequential Monte Carlo methods	13
Kalman filters and hidden Markov models	13
Resampling methods	13
Bootstrapping	13
Jackknifing	13
Random number generation	13
Probabilistic algorithms	13
Statistical paradigms	13
Queueing theory	13
Contingency table analysis	13
Regression analysis	13
Robust regression	13
Time series analysis	13
Survival analysis	13
Renewal theory	13
Dimensionality reduction	13
Cluster analysis	13
Statistical graphics	13
Exploratory data analysis	13
Stochastic processes	13
Markov processes	13
Nonparametric statistics	13
Distribution functions	13
Multivariate statistics	13
Mathematical software	13
Solvers	13
Statistical software	13
Mathematical software performance	13
Information theory	13
Coding theory	13
Mathematical analysis	13
Numerical analysis	13
Computation of transforms	13
Computations in finite fields	13
Computations on matrices	13
Computations on polynomials	13
Gröbner bases and other special bases	13
	-

Number-theoretic computations	13
Interpolation	13
Numerical differentiation	13
Interval arithmetic	13
Arbitrary-precision arithmetic	13
Automatic differentiation	13
Mesh generation	13
Discretization	13
Mathematical optimization	13
Discrete optimization	13
Network optimization	13
Continuous optimization	13
Linear programming	13
Semidefinite programming	13
Convex optimization	13
Quasiconvex programming and unimodality	13
Stochastic control and optimization	13
Quadratic programming	13
Nonconvex optimization	13
Mixed discrete-continuous optimization	13
Submodular optimization and polymatroids	13
Integer programming	13
Differential equations	13
Ordinary differential equations	13
Partial differential equations	13
Differential algebraic equations	13
Differential variational inequalities	13
Calculus	13
Lambda calculus	13
Differential calculus	13
Integral calculus	13
Functional analysis	13
Approximation	13
Integral equations	13
Nonlinear equations	13
Quadrature	13
Continuous mathematics	13
Calculus	13
Lambda calculus	13
Differential calculus	13
Integral calculus	13
Topology	13
Point-set topology	13
Algebraic topology	13
Geometric topology	13
Continuous functions	13
Information systems	
Data management systems	15
Database design and models	15
Relational database model	15
Entity relationship models	15
Graph-based database models	15
Hierarchical data models	15

Network data models	15
Physical data models	15
Data model extensions	15
Semi-structured data	15
Data streams	15
Data provenance	15
Incomplete data	15
Temporal data	15
Uncertainty	15
Inconsistent data	15
Data structures	15
Data access methods	15
Multidimensional range search	15
Data scans	15
Point lookups	15
Unidimensional range search	15
Proximity search	15
Data layout	15
Data compression	15
Data encryption	15
Record and block layout	15
Database management system engines	15
DBMS engine architectures	15
Database query processing	15
Query optimization	15
Query operators	15
Query planning	15
Join algorithms	15
Database transaction processing	15
Database transaction processing	15
Transaction logging	15
Database recovery	15
Record and buffer management	15
Parallel and distributed DBMSs	15
Key-value stores	15
MapReduce-based systems	15
Relational parallel and distributed DBMSs	15
Triggers and rules	15
Database views	15
Integrity checking	15
Distributed database transactions	15
Distributed database transactions Distributed data locking	15
Deadlocks	15
Distributed database recovery	15
Main memory engines	15
Online analytical processing engines	15
Stream management	15
	15
Query languages Relational database query languages	15
Structured Query Language	15
	15
XML query languages XPath	15
	15
XQuery	
Query languages for non-relational engines	<u>15</u> 15
MapReduce languages	15

Call level interfaces	15
Database administration	15
Database utilities and tools	15
Database performance evaluation	15
Autonomous database administration	15
Data dictionaries	15
Information integration	15
Deduplication	15
Extraction, transformation and loading	15
	15
Data exchange	15
Data cleaning	
Wrappers (data mining)	15
Mediators and data integration	15
Entity resolution	15
Data warehouses	15
Federated databases	15
Middleware for databases	15
Database web servers	15
Application servers	15
Object-relational mapping facilities	15
Data federation tools	15
Data replication tools	15
Distributed transaction monitors	15
Message queues	15
Service buses	15
Enterprise application integration tools	15
Middleware business process managers	15
Information storage systems	15
Information storage technologies	15
Magnetic disks	15
Magnetic tapes	15
Optical / magneto-optical disks	15
Storage class memory	15
Flash memory	15
Phase change memory	15
Disk arrays	15
Tape libraries	15
Record storage systems	15
Record storage alternatives	15
Heap (data structure)	15
Hashed file organization	15
Indexed file organization	15
Linked lists	15
Directory structures	15
B-trees	15
Vnodes	15
Inodes	15
Extent-based file structures	15
Block / page strategies	15
Slotted pages	15
Intrapage space management	15
Interpage free-space management	15
Record layout alternatives	15
Fixed length attributes	15
Variable length attributes	15
<b>`</b>	

	15
Null values in records Relational storage	15 15
	15
Horizontal partitioning	
Vertical partitioning	<u> </u>
Column based storage	
Hybrid storage layouts	15
Compression strategies	15
Storage replication	15
Mirroring	15
RAID	15
Point-in-time copies	15
Remote replication	15
Storage recovery strategies	15
Storage architectures	15
Cloud based storage	15
Storage network architectures	15
Storage area networks	15
Direct attached storage	15
Network attached storage	15
Distributed storage	15
Storage management	15
Hierarchical storage management	15
Storage virtualization	15
Information lifecycle management	15
Version management	15
Storage power management	15
Thin provisioning	15
Information systems applications	15
Enterprise information systems	15
Intranets	15
Extranets	15
Enterprise resource planning	15
Enterprise applications	15
Data centers	15
Collaborative and social computing systems and tools	15
Blogs	15
Wikis	15
Reputation systems	15
Open source software	15
Social networking sites	15
Social tagging systems	15
Synchronous editors	15
Asynchronous editors	15
Spatial-temporal systems	15
Location based services	15
Geographic information systems	15
Sensor networks	15
Data streaming	15
Global positioning systems	15
Decision support systems	15
Data warehouses	15
Expert systems	15
Data analytics	15
Online analytical processing	15
Mobile information processing systems	15
	1

Dragona control overterna	45
Process control systems	15
Multimedia information systems	15
Multimedia databases	15
Multimedia streaming	15
Multimedia content creation	15
Massively multiplayer online games	15
Data mining	15
Data cleaning	15
Collaborative filtering	15
Association rules	15
Clustering	15
Nearest-neighbor search	15
Data stream mining	15
Digital libraries and archives	15
Computational advertising	15
Computing platforms	15
World Wide Web	16
Web searching and information discovery	16
Web search engines	16
Web crawling	16
Web indexing	16
Page and site ranking	16
Spam detection	16
Content ranking	16
Collaborative filtering	16
Social recommendation	16
Personalization	16
Social tagging	16
Online advertising	16
Sponsored search advertising	16
Content match advertising	16
Display advertising	16
Social advertising	16
Web mining	16
Site wrapping	16
Data extraction and integration	16
Deep web	16
Surfacing	16
Search results deduplication	16
Web log analysis	16
Traffic analysis	16
Web applications	16
Internet communications tools	16
Email	16
Blogs	16
Texting	16
Chat	16
Web conferencing	16
Social networks	16
Crowdsourcing	16
Answer ranking	16
Trust	16
Incentive schemes	16
Reputation systems	16
Electronic commerce	16

Digital cash	16
Digital cash E-commerce infrastructure	16
Electronic data interchange	16
Electronic data interchange	16
Online shopping	16
Online banking	16
Secure online transactions	16
Online auctions	16
Web interfaces	16
Wikis	16
Browsers	
	16
Mashups Web services	16 16
Simple Object Access Protocol (SOAP)	16
RESTful web services	16
Web Services Description Language (WSDL)	16
Universal Description Discovery and Integration (UDDI)	16
Service discovery and interfaces	16
Web data description languages	16
Semantic web description languages	16
Resource Description Framework (RDF)	16
Web Ontology Language (OWL)	16
Markup languages	16
Extensible Markup Language (XML)	16
Hypertext languages	16
Information retrieval	17
Document representation	17
Document structure	17
Document topic models	17
Content analysis and feature selection	17
Data encoding and canonicalization	17
Document collection models	17
Ontologies	17
Dictionaries	17
Thesauri	17
Information retrieval query processing	17
Query representation	17
Query intent	17
Query log analysis	17
Query suggestion	17
Query reformulation	17
Users and interactive retrieval	17
Personalization	17
Task models	17
Search interfaces	17
Collaborative search	17
Retrieval models and ranking	17
Rank aggregation	17
Probabilistic retrieval models	17
Language models	17
Similarity measures	17
Learning to rank	17
Combination, fusion and federated search	17
Information retrieval diversity	17
Top-k retrieval in databases	17

Novelty in information ratioval	47
Novelty in information retrieval Retrieval tasks and goals	17
	17
Question answering	17
Document filtering	17
Recommender systems	
Information extraction	17
Sentiment analysis	17
Expert search	17
Near-duplicate and plagiarism detection	17
Clustering and classification	17
Summarization	17
Business intelligence	17
Evaluation of retrieval results	17
Test collections	17
Relevance assessment	17
Retrieval effectiveness	17
Retrieval efficiency	17
Presentation of retrieval results	17
Search engine architectures and scalability	17
Search engine indexing	17
Search index compression	17
Distributed retrieval	17
Peer-to-peer retrieval	17
Retrieval on mobile devices	17
Adversarial retrieval	17
Link and co-citation analysis	17
Searching with auxiliary databases	17
Specialized information retrieval	17
Structure and multilingual text search	17
Structured text search	17
Mathematics retrieval	17
Chemical and biochemical retrieval	17
	17
Multilingual and cross-lingual retrieval Multimedia and multimodal retrieval	17
	17
Image search	
Video search	17
Speech / audio search	17
Music retrieval	17
Environment-specific retrieval	17
Enterprise search	17
Desktop search	17
Web and social media search	17
	17
Security and privacy	
Cryptography	18
Key management	18
	18
Public key (asymmetric) techniques	18
Digital signatures	18
Public key encryption	
Symmetric cryptography and hash functions	18
Block and stream ciphers	18
Hash functions and message authentication codes	18
Cryptanalysis and other attacks	18
Information-theoretic techniques	18
Mathematical foundations of cryptography	18

Formal methods and theory of security	10
Trust frameworks	10
Security requirements	10
Formal security models	10
Logic and verification	10
Security services	19
Authentication	19
Biometrics	19
Graphical / visual passwords	19
Multi-factor authentication	19
Access control	19
Pseudonymity, anonymity and untraceability	19
Privacy-preserving protocols	19
Digital rights management	19
Authorization	19
Intrusion/anomaly detection and malware mitigation	19
Malware and its mitigation	19
Intrusion detection systems	19
Social engineering attacks	19
Spoofing attacks	19
Phishing	19
Security in hardware	19
Tamper-proof and tamper-resistant designs	19
Embedded systems security	19
Hardware security implementation	19
Hardware-based security protocols	19
Hardware attacks and countermeasures	19
Malicious design modifications	19
Side-channel analysis and countermeasures	19
Hardware reverse engineering	19
Systems security	19
Operating systems security	19
Mobile platform security	19
Trusted computing	19
Virtualization and security	19
Browser security	19
Distributed systems security	19
Information flow control	19
Denial-of-service attacks	19
Firewalls	19
Vulnerability management	19
Penetration testing	19
Vulnerability scanners	19
File system security	19
Network security	19
Security protocols	19
Web protocol security	19
Mobile and wireless security	19
Denial-of-service attacks	19
Firewalls	19
Database and storage security	19
Data anonymization and sanitization	19
Management and querying of encrypted data	19
Information accountability and usage control	19
Database activity monitoring	19
	I

Cottuers and employed an exercity	10
Software and application security	19
Software security engineering	19
Web application security	19
Social network security and privacy	19
Domain-specific security and privacy architectures	19
Software reverse engineering	19
Human and societal aspects of security and privacy	20
Economics of security and privacy	20
Social aspects of security and privacy	20
Privacy protections	20
Usability in security and privacy	20
there are the standard second to a	
Human-centered computing	
Human computer interaction (HCI)	20
HCI design and evaluation methods	20
User models	20
User studies	20
Usability testing	20
Heuristic evaluations	20
Walkthrough evaluations	20
Laboratory experiments	20
Field studies	20
Interaction paradigms	20
Hypertext / hypermedia	20
Mixed / augmented reality	20
Command line interfaces	20
Graphical user interfaces	20
Virtual reality	20
Web-based interaction	20
Natural language interfaces	20
Collaborative interaction	20
Interaction devices	20
Graphics input devices	20
Displays and imagers	20
Sound-based input / output	20
Keyboards	20
Pointing devices	20
Touch screens	20
Haptic devices	20
HCI theory, concepts and models	20
Interaction techniques	20
Auditory feedback	20
Text input	20
Pointing	20
Gestural input	20
Interactive systems and tools	20
User interface management systems	20
User interface programming	20
User interface toolkits	20
Empirical studies in HCI	20
Interaction design	20
Interaction design process and methods	20
User interface design	20
User centered design	20
	20

Activity centered design	20
Scenario-based design	20
Participatory design	20
Contextual design	20
Interface design prototyping	20
Interaction design theory, concepts and paradigms	20
Empirical studies in interaction design	20
Systems and tools for interaction design	20
Wireframes	20
Collaborative and social computing	21
Collaborative and social computing theory, concepts and	21
paradigms	21
Social content sharing	21
Collaborative content creation	21
Collaborative filtering	21
Social recommendation	21
Social networks	21
Social tagging	21
Computer supported cooperative work	21
Social engineering (social sciences)	21
Social navigation	21
Social media	21
	21
Collaborative and social computing design and evaluation methods	21
Social network analysis	21
Ethnographic studies	21
Collaborative and social computing systems and tools	21
Blogs	21
Wikis	21
	21
Reputation systems Open source software	21
Social networking sites	21
N N N N N N N N N N N N N N N N N N N	21
Social tagging systems Synchronous editors	21
	21
Asynchronous editors	21
Empirical studies in collaborative and social computing	
Collaborative and social computing devices	21
Ubiquitous and mobile computing	21
Ubiquitous and mobile computing theory, concepts and paradigms	21
Ubiquitous computing	21
Mobile computing	21
Ambient intelligence	21
Ubiquitous and mobile computing systems and tools	21
Ubiquitous and mobile devices	21
Smartphones	21
Interactive whiteboards	
	21 21
Mobile phones	
Mobile devices	21 21
Portable media players	
Personal digital assistants	21
Handheld game consoles	21
E-book readers	21
Tablet computers	21
Ubiquitous and mobile computing design and evaluation methods	21
Empirical studies in ubiquitous and mobile computing	21

Visualization	20
Visualization techniques	20
Treemaps	20
Hyperbolic trees	20
Heat maps	20
Graph drawings	20
Dendrograms	20
Cladograms	20
Visualization application domains	20
Scientific visualization	20
Visual analytics	20
Geographic visualization	20
Information visualization	20
	20
Visualization systems and tools Visualization toolkits	20
	20
Visualization theory, concepts and paradigms	20
Empirical studies in visualization	20
Visualization design and evaluation methods	
Accessibility	20
Accessibility theory, concepts and paradigms	20
Empirical studies in accessibility	20
Accessibility design and evaluation methods	20
Accessibility technologies	20
Accessibility systems and tools	20
Computing methodologies	
Symbolic and algebraic manipulation	12
Symbolic and algebraic algorithms	12
Combinatorial algorithms	12
Algebraic algorithms	12
Nonalgebraic algorithms	12
Symbolic calculus algorithms	12
Exact arithmetic algorithms	12
Hybrid symbolic-numeric methods	12
Discrete calculus algorithms	12
Number theory algorithms	12
Equation and inequality solving algorithms	12
Linear algebra algorithms	12
Theorem proving algorithms	12
	12
Boolean algebra algorithms	12
Optimization algorithms	12
Computer algebra systems	
Special-purpose algebraic systems	12
Representation of mathematical objects	12
Representation of exact numbers	12
Representation of mathematical functions	12
Representation of Boolean functions	12
Representation of polynomials	12
Parallel computing methodologies	12
Parallel algorithms	12
MapReduce algorithms	12
Self-organization	12
Shared memory algorithms	12
Vector / streaming algorithms	12
Massively parallel algorithms	12
Parallel programming languages	8

Artificial intelligence	22
Natural language processing	22
Information extraction	22
Machine translation	22
Discourse, dialogue and pragmatics	22
Natural language generation	22
Speech recognition	22
Lexical semantics	22
Phonology / morphology	22
Language resources	22
Knowledge representation and reasoning	22
Description logics	22
Semantic networks	22
Nonmonotonic, default reasoning and belief revision	22
Probabilistic reasoning	22
Vagueness and fuzzy logic	22
Causal reasoning and diagnostics	22
Temporal reasoning	22
Cognitive robotics	22
Ontology engineering	22
Logic programming and answer set programming	22
Spatial and physical reasoning	22
Reasoning about belief and knowledge	22
Planning and scheduling	22
Planning for deterministic actions	22
Planning under uncertainty	22
Multi-agent planning	22
Planning with abstraction and generalization	22
Robotic planning	22
Search methodologies	22
Heuristic function construction	22
Discrete space search	22
Continuous space search	22
Randomized search	22
Game tree search	22
Abstraction and micro-operators Search with partial observations	22
Control methods	22
Robotic planning	22
Computational control theory	22
Motion path planning	22
Philosophical/theoretical foundations of artificial intelligence	22
Cognitive science	22
Theory of mind	22
Distributed artificial intelligence	22
Multi-agent systems	22
Intelligent agents	22
Mobile agents	22
Cooperation and coordination	22
Computer vision	23
Computer vision tasks	23
Biometrics	23
Scene understanding	23
Activity recognition and understanding	23
Video summarization	23

Visual content-based indexing and retrieval	23
Visual inspection	23
Vision for robotics	23
Scene anomaly detection	23
Image and video acquisition	23
Camera calibration	23
Epipolar geometry	23
Computational photography	23
Hyperspectral imaging	23
Motion capture	23
3D imaging	23
Active vision	23
Computer vision representations	23
	23
Image representations	
Shape representations	23
Appearance and texture representations	23
Hierarchical representations	23
Computer vision problems	23
Interest point and salient region detections	23
Image segmentation	23
Video segmentation	23
Shape inference	23
Object detection	23
Object recognition	23
Object identification	23
Tracking	23
Reconstruction	23
Matching	23
Machine learning	24
Learning paradigms	24
Supervised learning	24
Ranking	24
Learning to rank	24
Supervised learning by classification	24
Supervised learning by regression	24
Structured outputs	24
Cost-sensitive learning	24
Unsupervised learning	24
Cluster analysis	24
Anomaly detection	24
Mixture modeling	24
<u> </u>	24
Topic modeling	
Source separation	24
Motif discovery	24
Dimensionality reduction and manifold learning	24
Reinforcement learning	24
Sequential decision making	24
Inverse reinforcement learning	24
Apprenticeship learning	24
Multi-agent reinforcement learning	24
Adversarial learning	24
Multi-task learning	24
Transfer learning	24
Lifelong machine learning Learning under covariate shift	24 24

Learning settings	24
Batch learning	24
Online learning settings	24
Learning from demonstrations	24
Learning from critiques	24
Learning from implicit feedback	24
Active learning settings	24
Semi-supervised learning settings	24
Machine learning approaches	24
Classification and regression trees	24
Kernel methods	24
Support vector machines	24
Gaussian processes	24
Neural networks	24
Logical and relational learning	24
Inductive logic learning	24
Statistical relational learning	24 24
Learning in probabilistic graphical models	24 24
Maximum likelihood modeling	24 24
v	24 24
Maximum entropy modeling Maximum a posteriori modeling	24 24
Mixture models	24 24
Latent variable models	24 24
	24 24
Bayesian network models	24 24
Learning linear models	24 24
Perceptron algorithm	24 24
Factorization methods	24 24
Non-negative matrix factorization Factor analysis	24 24
	24 24
Principal component analysis Canonical correlation analysis	24 24
Latent Dirichlet allocation	24 24
Rule learning	24 24
Instance-based learning	24 24
	24 24
Markov decision processes	
Partially-observable Markov decision processes	24
Stochastic games	24 24
Learning latent representations Deep belief networks	24 24
Machine learning algorithms	24
Dynamic programming for Markov decision processes Value iteration	24 24
	24 24
Q-learning Policy iteration	24 24
	24 24
Temporal difference learning	24 24
Approximate dynamic programming methods	
Ensemble methods	24
Boosting	24
Bagging	24
Spectral methods	24
Feature selection	24
Regularization	24
Cross-validation	24
Modeling and simulation	25
Model development and analysis	25

Modeling methodologies	25
Modeling methodologies Model verification and validation	25
Uncertainty quantification	25
Simulation theory	25
Systems theory	25
Network science	25
	25
Simulation types and techniques	25
Uncertainty quantification Quantum mechanic simulation	25
Molecular simulation	25
	25
Rare-event simulation	
Discrete-event simulation	25 25
Agent / discrete models	
Distributed simulation	25
Continuous simulation	25
Continuous models	25
Real-time simulation	25
Interactive simulation	25
Multiscale systems	25
Massively parallel and high-performance simulations	25
Data assimilation	25
Scientific visualization	25
Visual analytics	25
Simulation by animation	25
Simulation support systems	25
Simulation environments	25
Simulation languages	25
Simulation tools	25
Simulation evaluation	25
Computer graphics	26
Animation	26
Motion capture	26
Procedural animation	26
Physical simulation	26
Motion processing	26
Collision detection	26
Rendering	26
Rasterization	26
Ray tracing	26
Non-photorealistic rendering	26
Reflectance modeling	26
Visibility	26
Image manipulation	26
Computational photography	26
Image processing	26
Texturing	26
Image-based rendering	26
Antialiasing	26
Graphics systems and interfaces	26
Graphics systems and interfaces Graphics processors	26
Graphics processors Graphics input devices	26
Mixed / augmented reality	26
Perception	26
Graphics file formats	26
Virtual reality	26

Image compression	26
Shape modeling	26
Mesh models	26
Mesh geometry models	26
Parametric curve and surface models	26
Point-based models	26
Volumetric models	26
Shape analysis	26
Applied computing	
Electronic commerce	27
Digital cash	27
E-commerce infrastructure	27
Electronic data interchange	27
Electronic funds transfer	27
Online shopping	27
Online banking	27
Secure online transactions	27
Online auctions	27
Enterprise computing	27
Enterprise information systems	27
	27
Intranets	
Extranets	27
Enterprise resource planning	27
Enterprise applications	27
Data centers	27
Business process management	27
Business process modeling	27
Business process management systems	27
Business process monitoring	27
Cross-organizational business processes	27
Business intelligence	27
Enterprise architectures	27
Enterprise architecture management	27
Enterprise architecture frameworks	27
Enterprise architecture modeling	27
Service-oriented architectures	27
Event-driven architectures	27
Business rules	27
Enterprise modeling	27
Enterprise ontologies, taxonomies and vocabularies	27
Enterprise data management	27
Reference models	27
Business-IT alignment	27
	27
IT architectures	
IT governance	27
Enterprise computing infrastructures	27
Enterprise interoperability	27
Enterprise application integration	27
Information integration and interoperability	27
Physical sciences and engineering	27
Aerospace	27
Avionics	27
Archaeology	27

Astronomy	27
Chemistry	27
Earth and atmospheric sciences	27
Environmental sciences	27
Engineering	27
Computer-aided design	27
Physics	27
Mathematics and statistics	27
Electronics	27
Avionics	27
Telecommunications	27
Internet telephony	27
Life and medical sciences	28
Computational biology	28
Molecular sequence analysis	28
Recognition of genes and regulatory elements	28
Molecular evolution	28
Computational transcriptomics	28
Biological networks	28
Sequencing and genotyping technologies	28
	28
Imaging Computational proteomics	28
Molecular structural biology	28
Computational genomics	28
Genomics	28
Computational genomics	28
	28
Systems biology Consumer health	28
	28
Health care information systems Health informatics	28
Bioinformatics	28
Metabolomics / metabonomics	28
Genetics	28
Population genetics	28
Proteomics	28
Computational proteomics	28
Transcriptomics	28
	20
Law, social and behavioral sciences	29
Anthropology	29
Ethnography	
Law	29 29
Psychology	
Economics	29
Sociology	29
Computer forensics	29
Surveillance mechanisms	29
Investigation techniques	29
Evidence collection, storage and analysis	29
Network forensics	29
System forensics	29
Data recovery	29
Arts and humanities	29
Fine arts	29
Performing arts	29
Architecture (buildings)	29

Computer-aided design	29
Language translation	29
Media arts	29
	29
Sound and music computing Computers in other domains	29
Digital libraries and archives	29
•	29
Publishing	29 29
Military	29 29
Cyberwarfare	
Cartography	29
Geographic information systems	29
Agriculture	29
Computing in government	29
Voting / election technologies	29
E-government	29
Personal computers and PC applications	29
Word processors	29
Spreadsheets	29
Computer games	29
Microcomputers	29
Operations research	30
Consumer products	30
Industry and manufacturing	30
Supply chain management	30
Command and control	30
Computer-aided manufacturing	30
Decision analysis	30
Transportation	30
Forecasting	30
Marketing	30
Education	29
Digital libraries and archives	29
Computer-assisted instruction	29
Interactive learning environments	29
Collaborative learning	29
Learning management systems	29
Distance learning	29
E-learning	29
Computer-managed instruction	29
Document management and text processing	17
Document searching	17
Document management	17
Text editing	17
Version control	17
Document metadata	17
Document capture	17
Document analysis	17
Document scanning	17
Graphics recognition and interpretation	17
Optical character recognition	17
Online handwriting recognition	17
Document preparation	17
Markup languages	17
Extensible Markup Language (XML)	17
Hypertext languages	17
	1 ''

Annotation	17
Format and notation	17
Multi / mixed media creation	17
Image composition	17
Hypertext / hypermedia creation	17
Document scripting languages	17
Social and professional topics	
Management of computing and information systems	31
Project and people management	31
Project management techniques	31
Project staffing	31
Systems planning	31
Systems analysis and design	31
Systems development	31
Computer and information systems training	31
Implementation management	31
Hardware selection	31
Computing equipment management	31
Pricing and resource allocation	31
Software management	31
Software maintenance	31
Software selection and adaptation	31
System management	31
Centralization / decentralization	31
Technology audits	31
Quality assurance	31
	31
Network operations	31
File systems management	31
Information system economics	31
History of computing	32
Historical people	32
History of hardware	32
History of software	32
History of programming languages	32
	32
History of computing theory	32
Computing education	
Computational thinking	32
Accreditation	32
Model curricula	32
Computing education programs	32
Information systems education	32
Computer science education	32
Computer science education Computer engineering education	32 32
Computer science education Computer engineering education Information technology education	32 32 32
Computer science education Computer engineering education Information technology education Information science education	32 32 32 32 32
Computer science education         Computer engineering education         Information technology education         Information science education         Computational science and engineering education	32 32 32 32 32 32 32
Computer science education         Computer engineering education         Information technology education         Information science education         Computational science and engineering education         Software engineering education	32 32 32 32 32 32 32 32 32
Computer science education         Computer engineering education         Information technology education         Information science education         Computational science and engineering education	32 32 32 32 32 32 32 32 32 32
Computer science education         Computer engineering education         Information technology education         Information science education         Computational science and engineering education         Software engineering education	32 32 32 32 32 32 32 32 32
Computer science education         Computer engineering education         Information technology education         Information science education         Computational science and engineering education         Software engineering education         Informal education	32 32 32 32 32 32 32 32 32 32
Computer science education         Computer engineering education         Information technology education         Information science education         Computational science and engineering education         Software engineering education         Informal education         Computer engineering education	32         32         32         32         32         32         32         32         32         32         32         32         32         32         32         32         32         32         32         32

Any other topics		
Any topic which does not fit into the above categories	33	